

Abstract

An interface circuit comprising one or two input branches and one output branch, each branch being connected between upper and lower supply terminals, each input branch comprising a transistor having its control electrode connected to the input of the
5 interface circuit, one of the two other electrodes of the transistor being connected to one of the supply terminals, a current source being placed between the other one of the supply terminals and an intermediary node connected to the last transistor electrode possibly via one or several diodes, the output branch comprising two complementary transistors having their control electrodes connected to the intermediary nodes of one of the input
10 branches or to the circuit input, one of the electrodes of each of the complementary transistors being connected to the circuit output, the last electrode of each of the transistors being connected to a supply terminal.